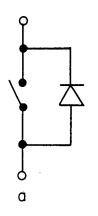
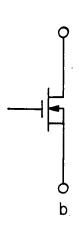
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FIG. I





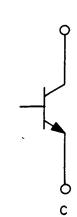
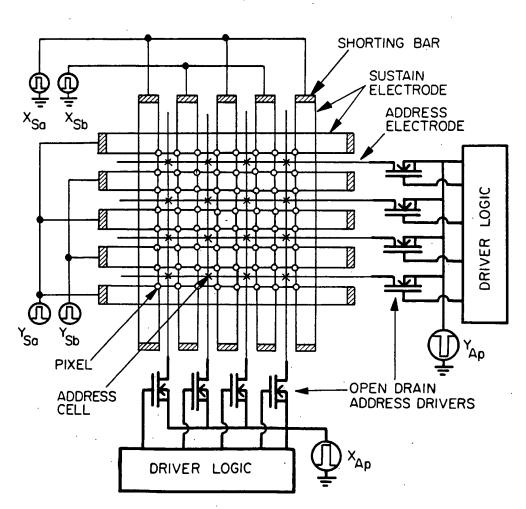
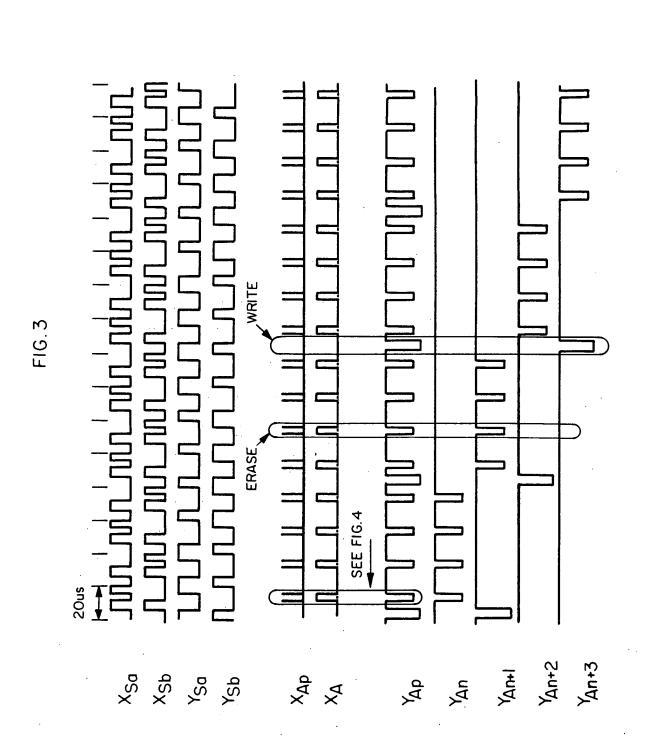


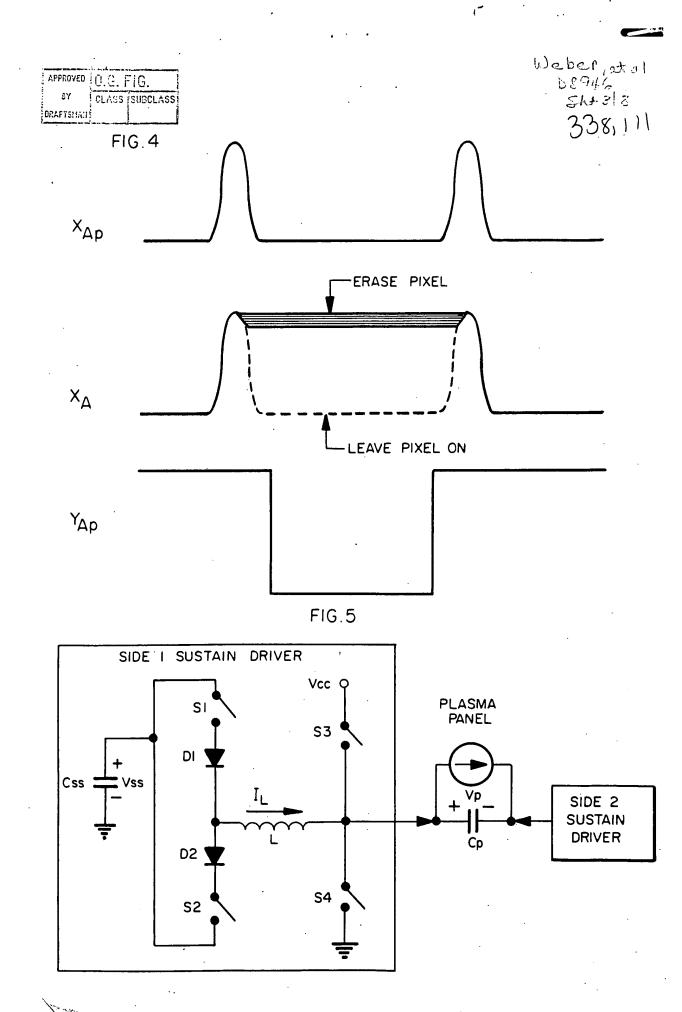
FIG. 2

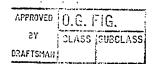


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BY	CLASS	SUBCLASS	
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Weber et al. 58946. 58-2/8 338/11/







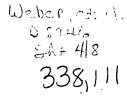


FIG. 6

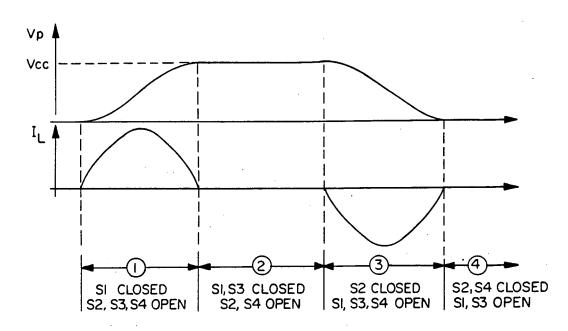


FIG.7 SI CI : Vcc RI Ιı Dcl ۷ı PLASMA PANEL S3 ٧ss DI : Cdl ₹R3 Css ≹ RdI Vρ SIDE 2 SUSTAIN DRIVER V_L RL RL Ср **S4 D**2 **-** C4 Cd2 }R4 ₹Rd2 V2 Dc2 S2 **c**2 { R2

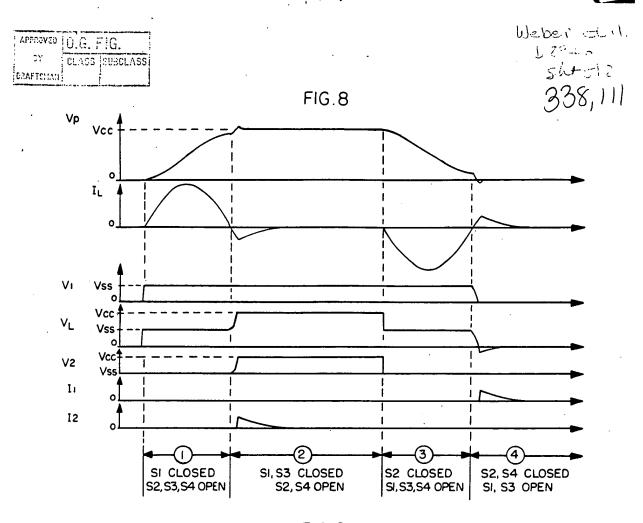
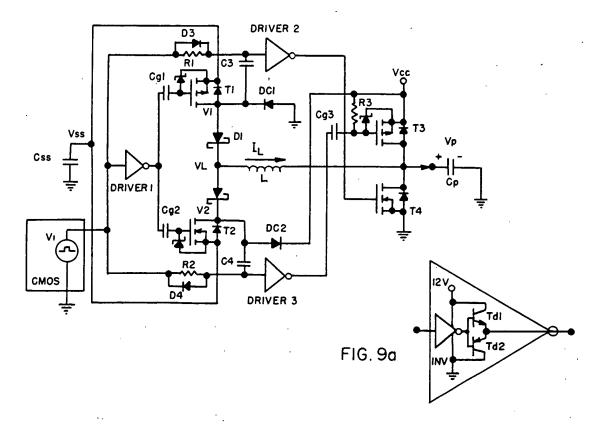


FIG.9



10-8746 338/11/ PROVED C.G. FIG. ΞY CLASS (SUBCLASS) AFTSMAN SIDE 2 SUSTAIN DRIVER PLASMA PANEL م + 11-<u>:</u> ප FIG. 10 -||10 150 ა გ **L 1** D2 Cssı Vss 1 -|11-TTL OR CMOS LOGIC <u>;</u> Ϊ̈́

APPROVED	: 11 C		
	U.G. FIG.		
CRAFTSMAN	GLANS	SUBCLASS	
-		į	

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FIG.II

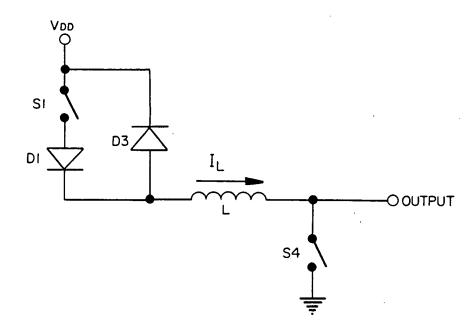
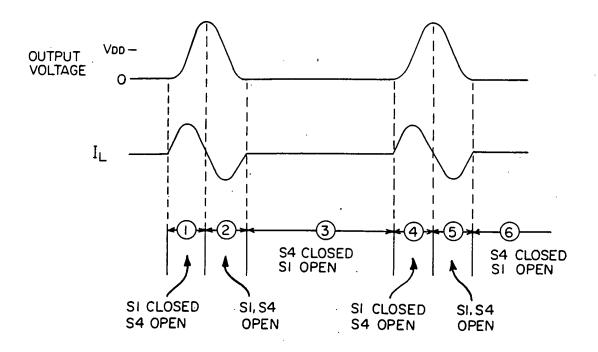


FIG. 12



APPROVED O.G. FIG.

SY CLASS SUBCLASS
DRAFTSMAH

FIG. 13

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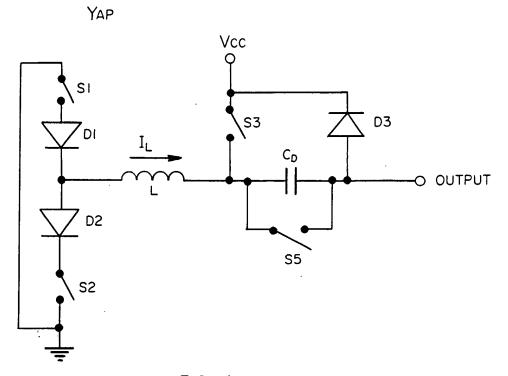


FIG. 14

